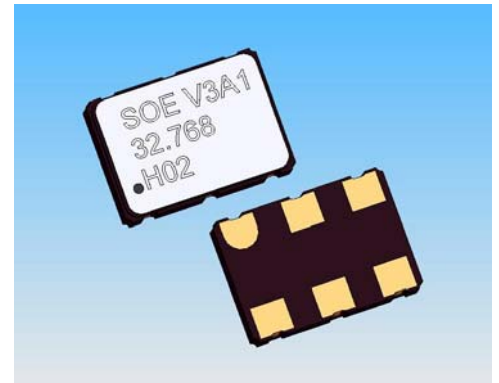


# V Type VCXO

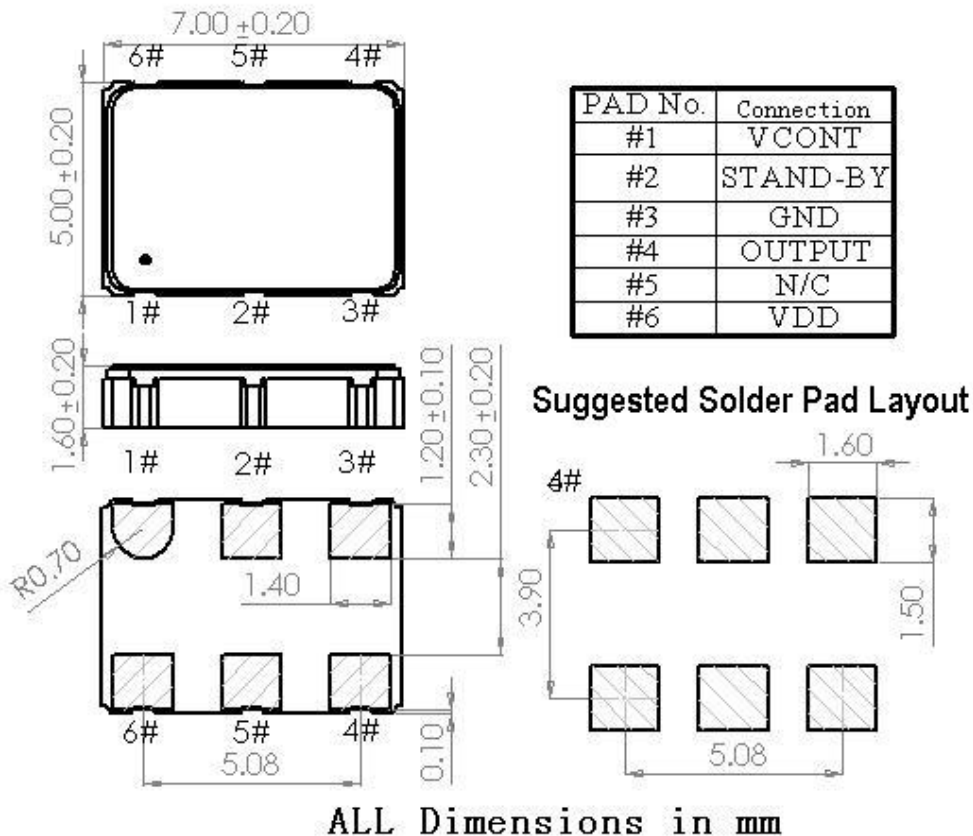
## Features

- CMOS/TTL Compatible VCXO.
- Typical 7.0 x 5.0 x 1.6 mm ceramic metal SMD package.
- Resistance Seam welding for hermetical seal.
- Tape & Reel packaging for automatic assembly.
- **Lead Free and RoHS compliant**



## ORDERING GUIDANCE

Product Type	Supply Voltage (V)	Frequency Stability (ppm)	Operating Temp. Range (°C)	OE PIN	—	APR	—	Frequency (MHz)
Code: Meaning	Code: Meaning	Code: Meaning	Code: Meaning	Code: Meaning	Dash		Dash	
V: 7X5 SMD CMOS/TTL VCXO	3: 3.3 5: 5.0	A: ±25 B: ±50 D: ±20 G: ±15 H: ±30	1: 0~+70 2: -40~+85 3: 0~+85 4: -20~+70 5: -10~+85 6: -10~+70 7: -10~+60 8: 0~+60	Default: pin2 5: pin5		Specify desired APR Value In ppm		XXX.XXXXXX
<b>Ordering example:</b> V3A4-100-32.768MHz 7x5 SMD CMOS/TTL VCXO, 3.3V, ±25ppm, 0°C~70°C, OE@PIN2, APR=±100ppm, 32.768MHz								
<b>Note:</b> If there're any other particular requirements, they should be specified in customers' order.								



## Electrical Specification

Parameter	Min		Max		Unit
	V <sub>DD</sub> =3.3V	V <sub>DD</sub> =5.0V	V <sub>DD</sub> =3.3V	V <sub>DD</sub> =5.0V	
Supply Voltage (V <sub>DD</sub> )±10%	2.97	4.5	3.63	5.5	V
Frequency Range	1		80	50	MHz
Frequency Stability (V <sub>c</sub> =V <sub>DD</sub> /2)	Refer to Ordering Guidance (Overall condition Inclusive of calibration @ 25 °C , operating temperature change, V <sub>DD</sub> variation, load variation, aging, etc.)				ppm
Operating Temperature Range	Refer to Ordering Guidance				°C
Absolute Pull Range (APR)	±50, ±75, ±100 typ. Maximum APR of ±150PPM available dependent on Frequency				ppm
Control Voltage Range	0	0.5	3.3	4.5	V
Linearity	-		10		%
Supply Current					mA
1MHz =<F <sub>o</sub> <20MHz	-		10	20	
20MHz =<F <sub>o</sub> <50MHz	-		15	25	
50MHz =<F <sub>o</sub> <80MHz	-		25	-	
Duty Cycle (CMOS)	45		55		%
Output High (V <sub>oh</sub> )	0.9V <sub>DD</sub>		-		V
Output Low (V <sub>ol</sub> )	-		0.1V <sub>DD</sub>		
Rise Time/Fall Time (tr/tf)	Measured between 10% to 90% of V <sub>DD</sub> with output load of 15pF				nS
1MHz =<F <sub>o</sub> <20MHz	-		6	5	
20MHz =<F <sub>o</sub> <50MHz	-		5	4	
50MHz =<F <sub>o</sub> <80MHz	-		3	-	
Output Load(Fan-out) (CMOS)	15	50	-		pF
Tristate Function					V
Output Enable	2	4	(With OE PIN floating, Output enabled)		
Output Disable(Hi-Z)			0.5	0.8	
Start Time	-		10		mS
DC Input Impedance @ V <sub>c</sub> pin	2		-		Mohm
Modulation Bandwidth	10		-		KHz
Phase Jitter(RMS, 12KHz to 20MHz)	-		1		pS
Storage Temperature	-55		125		°C