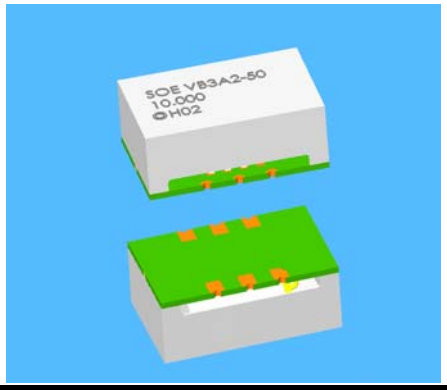
VB Type VCXO

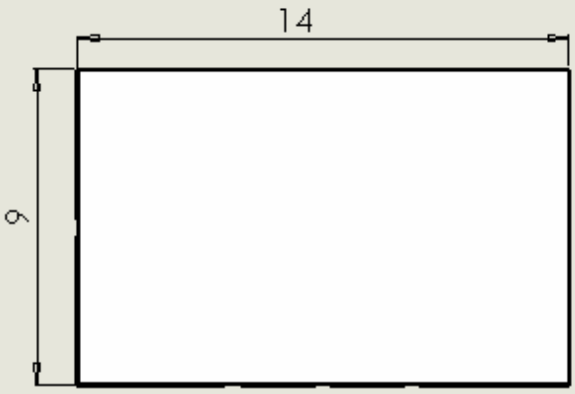


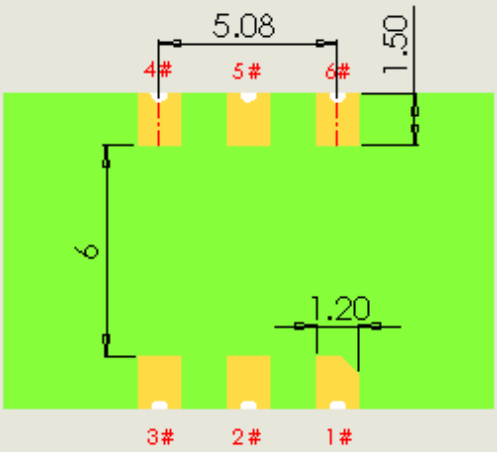
Features

- **CMOS/TTL Compatible VCXO.**
- **Typical 9.0 x 14.0 x 5.5 mm SMD package.**
- **UM1 or UM5 or SMD3.5x6 crystal used interiorly.**
- **Lead Free and RoHS compliant**

ORDERING GUIDANCE

Product Type	Supply Voltage (V)	Frequency Stability (ppm)	Operating Temp. Range (℃)	OE PIN	—	APR	—	Frequency (MHz)
Code: Meaning	Code: Meaning	Code: Meaning	Code: Meaning	Code: Meaning	Dash		Dash	
VB: 9X14 SMD CMOS/TTL VCXO	**3:** 3.3 **5:** 5.0	**A:** ±25 **B:** ±50 **D:** ±20 **G:** ±15 **H:** ±30	**1:** 0~+70 **2:** -40~+85 **3:** 0~+85 **4:** -20~+70 **5:** -10~+85 **6:** -10~+70 **7:** -10~+60 **8:** 0~+60	**Default:** pin2 **5:** pin5		**Specify desired APR Value In ppm**		XXX.XXXXXX
Ordering example:VB3A4-100-32.768MHz 9x14 SMD CMOS/TTL VCXO, 3.3V, ±25ppm, -20℃~70℃, OE@PIN2, APR=±100ppm, 32.768MHz								
V 批 Note: If there're any other particular requirements, they should be specified in customers' order.								





PIN	FUNCTION
1	VCON
2	E/D or NC
3	GND
4	OUTPUT
5	NC
6	VDD

Electrical Specification

Parameter	Min		Max		Unit
Supply Voltage (V_{DD})±10%	**V_{DD}=3.3V**	**V_{DD}=5.0V**	**V_{DD}=3.3V**	**V_{DD}=5.0V**	
	2.97	4.5	3.63	5.5	**V**
Frequency Range	1		80	50	**MHz**
Frequency Stability ($Vc=V_{DD}/2$)	Refer to Ordering Guidance (Overall condition Inclusive of calibration @ 25℃, operating temperature change, V_{DD} variation, load variation, aging, etc.)				**ppm**
Operating Temperature Range	Refer to Ordering Guidance				℃
Absolute Pull Range (APR)	±50, ±75, ±100 typ. Maximum APR of ±150PPM available dependent on Frequency				**ppm**
Control Voltage Range	0	0.5	3.3	4.5	**V**
Linearity	–		10		%
Supply Current					**mA**
1MHz =<Fo<20MHz	–		10	20	
20MHz =<Fo<50MHz	–		15	25	
50MHz =<Fo<80MHz	–		25	–	
Duty Cycle (CMOS)	45		55		%
Output High (Voh)	$0.9V_{DD}$		–		**V**
Output Low (Vol)	–		$0.1V_{DD}$		
Rise Time/Fall Time (tr/tf)	Measured between 10% to 90% of V_{DD} with output load of 15pF				**nS**
1MHz =<Fo<20MHz	–		6	5	
20MHz =<Fo<50MHz	–		5	4	
50MHz =<Fo<80MHz	–		3	–	
Output Load(Fan-out) (CMOS)	15		50		**pF**
Tristate Function					
Output Enable	2	4	(With OE PIN floating, Output enabled)		**V**
Output Disable(Hi-Z)			0.5	0.8	
Start Time	–		10		**mS**
DC Input Impedance @ Vc pin	2		–		**Mohm**
Modulation Bandwidth	10		–		**KHz**
Phase Jitter(RMS, 12KHz to 20MHz)	–		1		**pS**
Storage Temperature	-55		125		℃