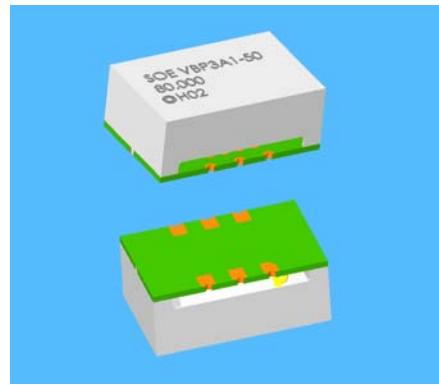


VBP Type VCXO

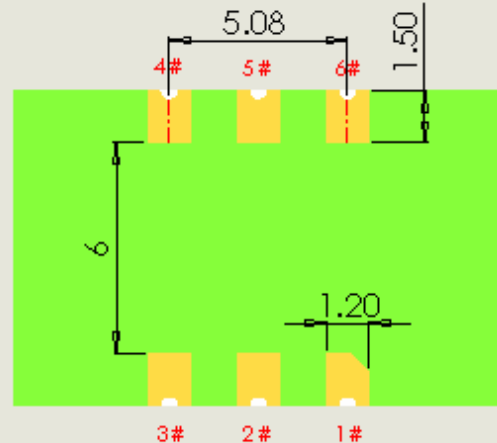
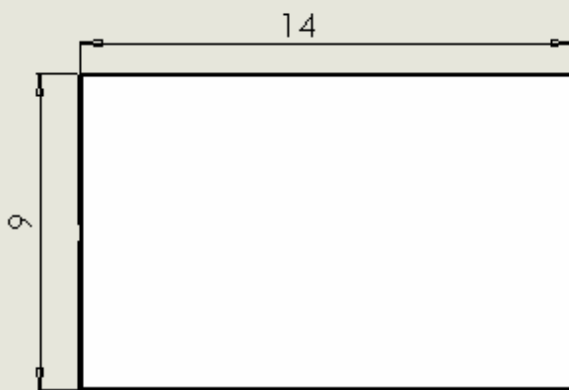
Features

- LVPECL differential output VCXO.
- Typical 9.0 x 14.0 x 5.5 mm SMD package.
- UM1 or UM5 or SMD3.5x6 crystal used interiorly.
- Low jitter performance suited for synchronous system.
- **Lead Free and RoHS compliant**



ORDERING GUIDANCE

Product Type	Supply Voltage (V)	Frequency Stability (ppm)	Operating Temp. Range (°C)	—	APR	—	Frequency (MHz)
Code: Meaning	Code: Meaning	Code: Meaning	Code: Meaning	Dash		Dash	
VBP: 9X14 SMD LVPECL VCXO	2: 2.5 3: 3.3	A: ±25 B: ±50 D: ±20 H: ±30	1: 0~+70 2: -40~+85 3: 0~+85 4: -20~+70 5: -10~+85 6: -10~+70		Specify desired APR Value In ppm		XXX.XXXXXX
Ordering example: VBP3A4-50-77.760MHz 9x14 SMD PECL VCXO, 3.3V, ±25ppm, -20°C~70°C, APR=±50ppm, 77.760MHz							
Note: If there're any other particular requirements, they should be specified in customers' order.							



PIN	FUNCTION
1	VCON
2	E/D or NC
3	GND
4	OUTPUT
5	OUTPUT
6	VDD

Electrical Specification

Parameter	Min		Max		Unit
	V _{DD} =2.5V	V _{DD} =3.3V	V _{DD} =2.5V	V _{DD} =3.3V	
Supply Voltage (V _{DD})±10%	2.25	2.97	2.75	3.63	V
Frequency Range	1		80		MHz
Frequency Stability (V _c =V _{DD} /2)	Refer to Ordering Guidance (Overall condition Inclusive of calibration @ 25 °C , operating temperature change, V _{DD} variation, load variation, aging, etc.)				ppm
Operating Temperature Range	Refer to Ordering Guidance				°C
Absolute Pull Range (APR)	±50, ±75, ±100 typ.				ppm
Control Voltage Range	0	0	2.5	3.3	V
Linearity	-		10		%
Supply Current	Output load 50 ohm terminated to (V _{DD} -2.0V)				mA
1MHz =<F _o <20MHz	-		50		
20MHz =<F _o <80MHz	-		75		
Duty Cycle (@ 50% of wf)	45		55		%
Output High (V _{oh})	1.475	2.275	-	-	V
Output Low (V _{ol})	-	-	1.095	1.68	
Rise Time/Fall Time (tr/tf)	Measured between 20% to 80% of the output waveform				nS
1MHz =<F _o <20MHz	-		1.5		
20MHz =<F _o <80MHz	-		1		
Output Load	50 ohm terminated to (V _{DD} -2.0V)				
Tristate Function(input to pin2)					V
Output Enable	2	2.5	(With pin2 floating, Output enabled)		
Output Disable(Hi-Z)			0.5	0.5	
Start Time	-		10		mS
DC Input Impedance @ V _c pin	2		-		Mohm
Modulation Bandwidth	10		-		KHz
Phase Jitter(RMS, 12KHz to 20MHz)	-		1		pS
Storage Temperature	-55		125		°C